Applicant: Kazutaka Inukai Attorney's Docket No.: 12732-201001 / US6877

Serial No. : 10/743,344

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## Amendments to the Specification:

Please replace the paragraph beginning at page 1, line 1 through the end of the application, including the abstract with the attached substitute specification in English.

## Please replace the abstract at page 28 with the following amended abstract:

-{Object} The invention provides a current data amplifier circuit of which output current value is accurate even when transistors with large variation in electrical characteristics are used. [Solving Means] According to the invention, a plurality of transistors configuring a driving element is used in a series connection state when inputting current and in a parallel connection state when outputting current. Thus, is it significantly suppressed that the variation are not reflected on the output current value even when the plurality of transistors configuring the driving element have variation.